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emulation sleep clocks "device under test" lock

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C Nemecek - US Patent 6,922,821, 2005 - [freepatentsonline.com](#)

... of the FPGA implementation of an **emulator** for the ... or when system encounters a breakpoint (eg, **sleep**, stall, internal ... However, using a **clock**, which is two times ...

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[Combined in-circuit emulator and programmer - all 2 versions »](#)

C Nemecek, S Roe - US Patent 7,089,175, 2006 - [Google Patents](#)

... to the base station, the interface having a **clock** signal me ... microcontroller can be placed in a **sleep** mode so ... use with a combined In-Circuit **Emulation** system and ...

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C Nemecek, S Roe - US Patent 7,236,921, 2007 - [Google Patents](#)

... If the microcontroller is not in a **sleep** mode, the ... An In-Circuit **Emulation** system consistent with certain trailer perspective. having a microcontroller **clock**. ...

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C Nemecek, S Roe - US Patent 7,162,410, 2007 - [Google Patents](#)

... The In-Circuit **Emulation** system has a microcontroller operating in **lock-step** synchronization with ... operates with its own gatekeeper **clock** independent of the ...

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... validated using **emulation** techniques-running test scripts that mimic the signaling ... The ESG-D **clock** ... during standby and **sleep** modes. DWDM ...

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[BOOK] [Automobile Electrical and Electronic Systems](#)

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G Comments - Hinckley Journal OF Politics - analog.com

... to derive the primary channel's master **clock** while the ... off for existing FM receivers
to **lock** to), they ... was told stood for general electro O ptical **emulator**. ...

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LMA Jalloul - EURASIP Journal on Applied Signal Processing, 2005 - hindawi.com

... The design was built with additional context and compute resources just in case
that the time slicing can be increased by higher **clock** speed and better silicon ...

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[CITATION] DARET: A Logic-Based Language in Object-Oriented Databases

TW Chan, WC Wang - SEKE'93: The 5th International Conference on Software ..., 1993 - Knowledge Systems
Institute

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